



uC/OS-III: The Real-Time Kernel and the Texas Instruments Stellaris MCUs

Jean J. Labrosse

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This book puts the spotlight on how a real-time kernel works using Micrium's $\mu\text{C}/\text{OS-III}$ as a reference. The book consists of two complete parts. The first describes real-time kernels in generic terms. Part II provides examples for the reader, using Texas Instruments' EVM-EVALBOT, a small, robotic evaluation board. The board is based on the Stellaris LM3S9B92 which combines the popular ARM Cortex-M3[®] architecture with Ethernet MAC+PHY, USB OTG (On-The-Go), and I2S. Together with the IAR Systems Embedded Workbench for ARM development tools, the evaluation board provides everything necessary to enable the reader to be up and running quickly, as well as a fun and educational experience, resulting in a high-level of proficiency in a short time.

This book is written for serious embedded systems programmers, consultants, hobbyists, and students interested in understanding the inner workings of a real-time kernel. $\mu\text{C}/\text{OS-III}$ is not just a great learning platform, but also a full commercial-grade software package, ready to be part of a wide range of products.

$\mu\text{C}/\text{OS-III}$ is a highly portable, ROMable, scalable, preemptive real-time, multitasking kernel designed specifically to address the demanding requirements of today's embedded systems. $\mu\text{C}/\text{OS-III}$ is the successor to the highly popular $\mu\text{C}/\text{OS-II}$ real-time kernel but can use most of $\mu\text{C}/\text{OS-II}$'s ports with minor modifications. Some of the features of $\mu\text{C}/\text{OS-III}$ are:

Preemptive multitasking with round-robin scheduling of tasks at the same priority

Supports and unlimited number of tasks and other kernel objects

Rich set of services: semaphores, mutual exclusion semaphores with full priority inheritance, event flags, message queues, timers, fixed-size memory block management, and more. Built-in performance measurements

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